

Compal Confidential

Schematics Document

INTEL Auburndale BGA with IBEX core logic

Fossil 2.0 UMA

LA-6161P

2010-05-24

REV:1.0

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XDP Conn.

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Accelerometer

LIS302DLTR

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Fan Control

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DDR3-SO-DIMM X 1

BANK 2, 3

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DDR3 1066/1333MHz 1.5V

Single Channel

Mobile

Arrandale CPU

BGA 1288pins

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LVDS

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Display port

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CRT

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DDI_D

FDI

DMI X4

DDI

WWAN
+SIM Card
USB*1

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USB2.0

PCI-E BUS

10/100/1000 LAN
RTL8151DH-GR

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WLAN Card

PCIE*1

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RJ45 CONN

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Intel Ibex Peak M

1071pins

25mm*27mm

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ONFI Interface

USB2.0

Azalia

SATA0

BT(SoftBreeze) Conn USB x 1

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USB conn x 3(For I/O)

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daughter board

CardReader Controller
RealTek RTS5159

SD/MMC Slot

sub/B Page 3

USB x1(Camara)

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FPR conn x1

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Audio CKT

IDT 92HD80

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daughter board

Audio Jack

sub/B Page 2

SATA HDD Connector

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SPI BUS

SMSC KBC 1098

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RTC CKT.

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LED

LED Board

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Power OK CKT.

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Power On/Off CKT.

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DC/DC Interface CKT.

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Touch Pad CONN.

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Int.KBD

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SPI ROM

4MB

MX25L6445EM2I-10G Page 27

SPI BUS

CK505

Clock Generator
SLG8SP585VTR

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Voltage Rails (O MEANS ON X MEANS OFF)

power plane State	+RTCVCC	+B +3VL	+5VALW +3VALW	+1.5V +0.75V	+5VS +3VS +1.5VS +VCCP +CPU_CORE +1.05VS +1.8VS	
S0	O	O	O	O	O	
S1	O	O	O	O	O	
S3	O	O	O	O	X	
S5 S4/AC	O	O	O	X	X	
S5 S4/ Battery only	O	O	X	X	X	
S5 S4/AC & Battery don't exist	O	X	X	X	X	

Symbol Note :

 : means Digital Ground

 : means Analog Ground

@ : means just reserve , no build

ULV@ : means just install for ULV CPU

CONN@ : means ME part.

 Layout Notes

 07/24 update

 : Question Area Mark.(Wait check)

Install below 45 level BOM structure for ver. 0.1

45@ : means just put it in the BOM of 45 level.

Install below 43 level BOM structure for ver. 0.1

DEBUG@ : means just build when PCIE port 80 CARD function enable *Remove before MP*

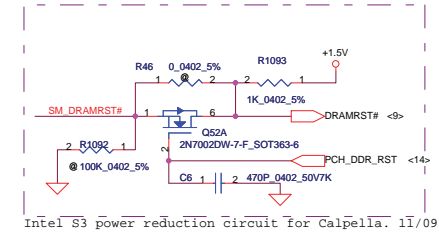
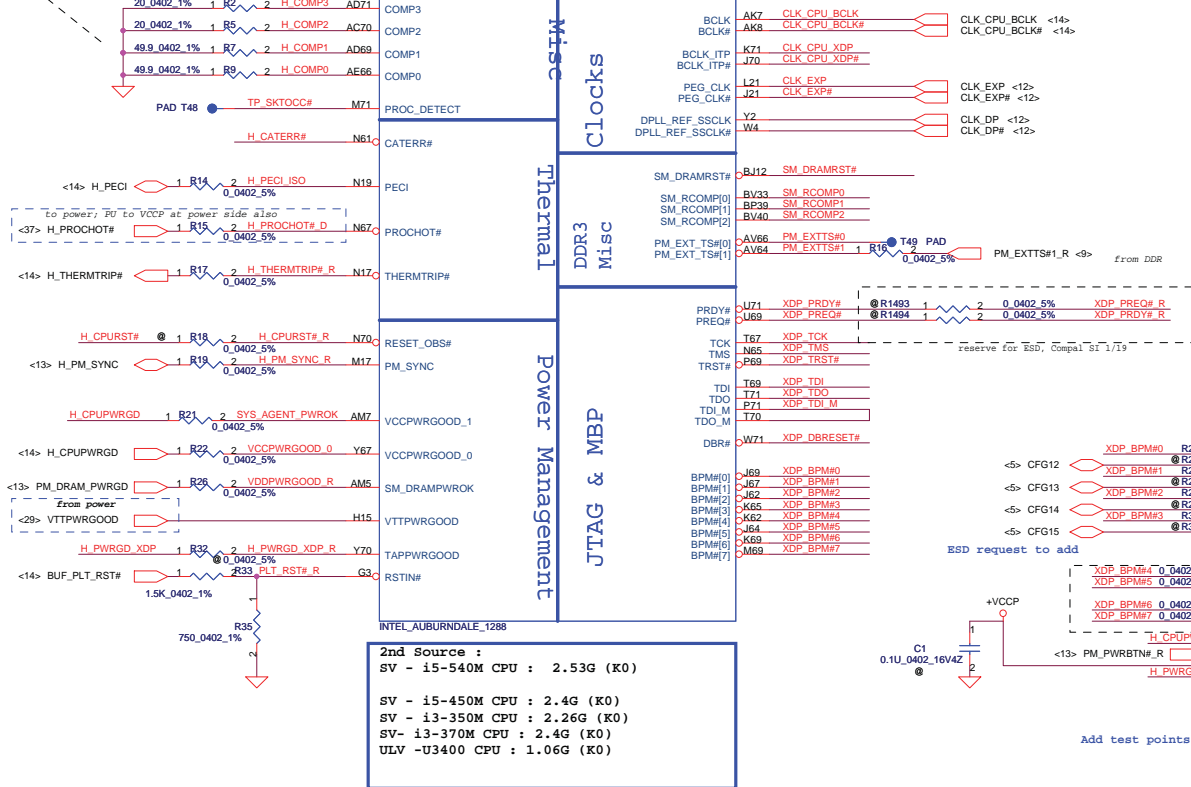
SMBUS Control Table

	SOURCE	BATT	XDP	SODIMM	CLK CHIP	MINI CARD	DOCK	NIC	THERMAL SENSOR	G-SENSOR
SMB_EC_CK1 SMB_EC_DAI	SMSC1098	V	X	X	X	X	X	X	X	X
SMBCLK SMBDATA	Calpella	X	V	V	V	V	V	X	X	V
SML0CLK SML0DATA	Calpella	X	X	X	X	X	X	V	X	X
SML1CLK SML1DATA	Calpella	X	X	X	X	X	X	X	V	V

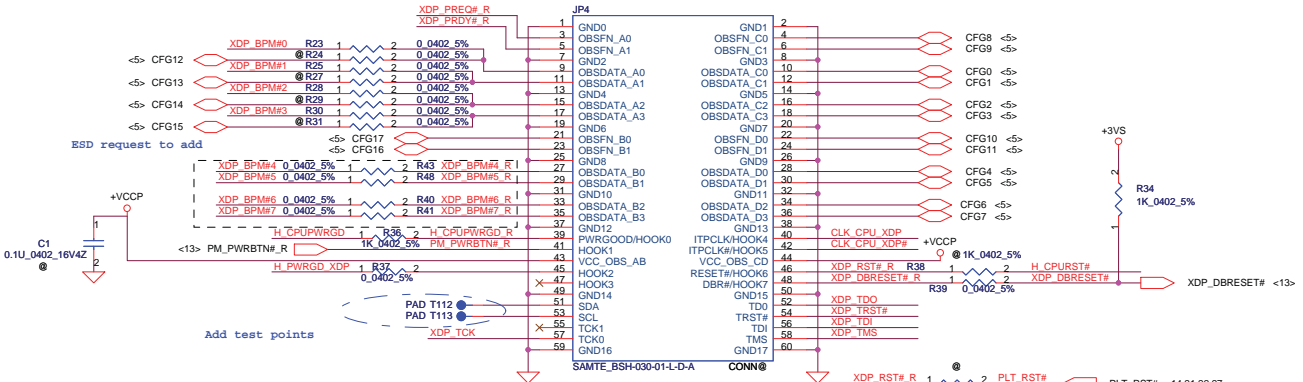
Strapping Options Flash

CNT1#	CNT0#	Routing
0	1	Reserved
1	0	Flash Cycles Routed to PCI
1	1	Flash Cycles Routed to SPI
0	0	Flash Cycles Routed to LPC

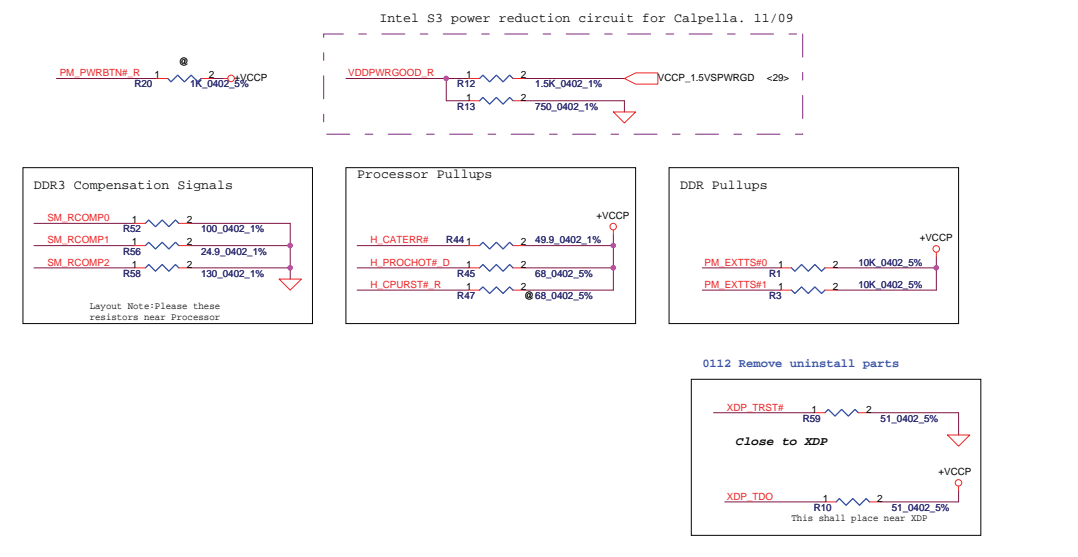
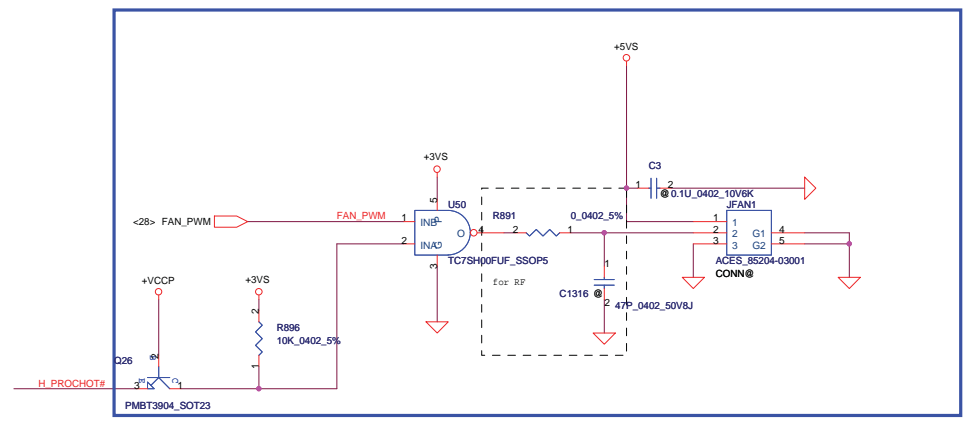
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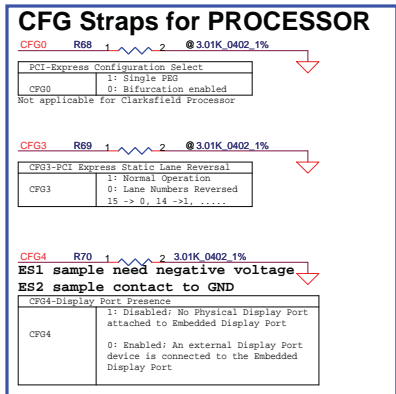
CPU XDP Connector



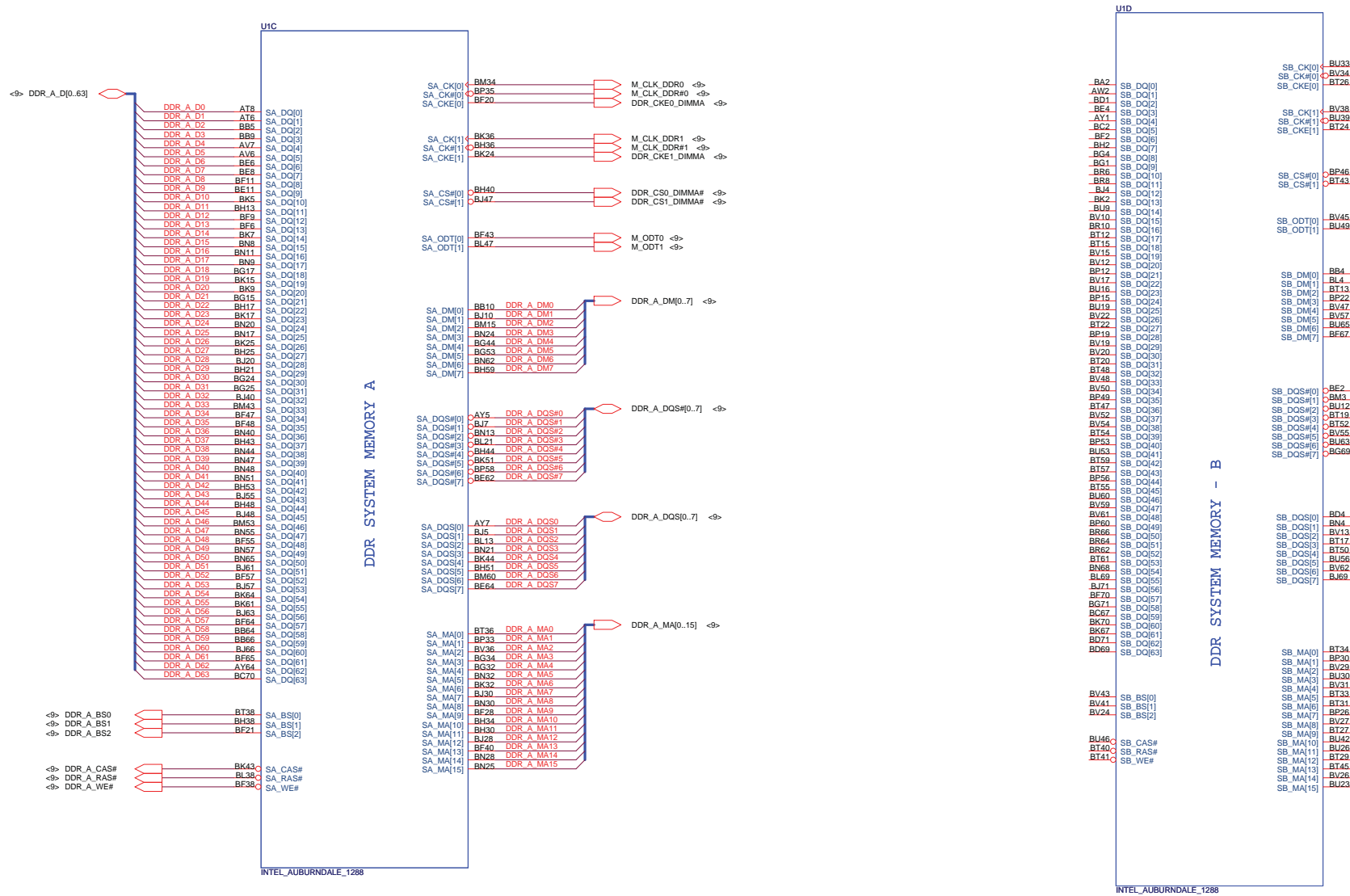
PWM Fan Control circuit



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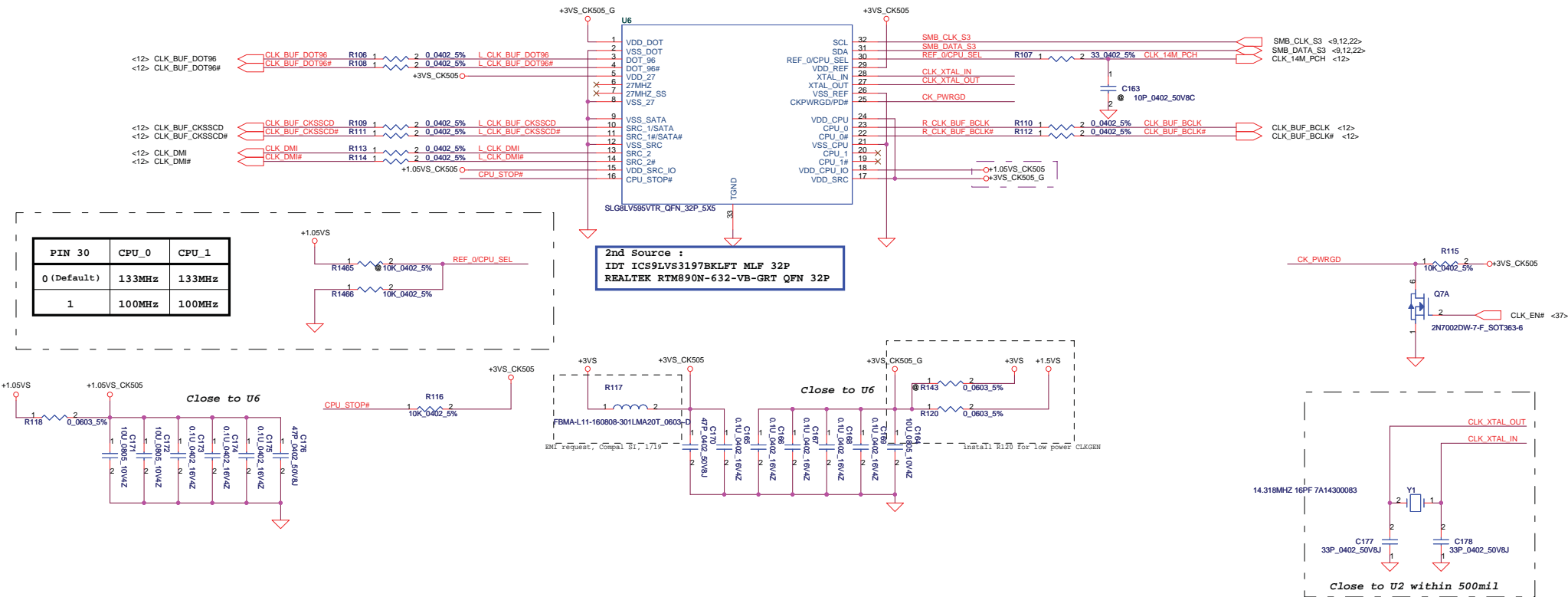
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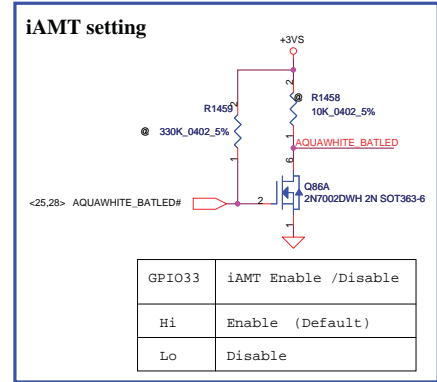
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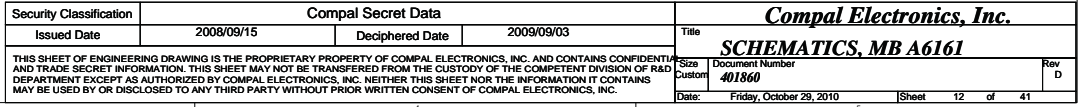
U1I			U1J		
BU62	VSS1	VSS90	AY23		
BU58	VSS2	VSS91	AY21		
BU55	VSS3	VSS92	AY19		
BU51	VSS4	VSS93	AY17		
BU48	VSS5	VSS94	AH50		
BU44	VSS6	VSS95	AH48		
BU37	VSS7	VSS96	AH47		
BU32	VSS8	VSS97	AH46		
BU25	VSS9	VSS98	AH44		
BU21	VSS10	VSS99	AH42		
BU18	VSS11	VSS100	AH41		
BU14	VSS12	VSS101	AH39		
BU11	VSS13	VSS102	AH37		
BU7	VSS14	VSS103	AH36		
BP42	VSS15	VSS104	AH33		
BL7	VSS16	VSS105	AH32		
BN64	VSS17	VSS106	AH30		
BN6	VSS18	VSS107	AH28		
BM70	VSS19	VSS108	AH26		
BM51	VSS20	VSS109	AH24		
BM44	VSS21	VSS110	AH23		
BM32	VSS22	VSS111	AH21		
BM24	VSS23	VSS112	AH19		
BM17	VSS24	VSS113	AH17		
BL57	VSS25	VSS114	AH15		
BL55	VSS26	VSS115	AH14		
BL48	VSS27	VSS116	AG64		
BL40	VSS28	VSS117	AG62		
BL28	VSS29	VSS118	AG6		
BL20	VSS30	VSS119	AF69		
BK63	VSS31	VSS120	AF62		
BK60	VSS32	VSS121	AF1		
BK53	VSS33	VSS122	AE70		
BK34	VSS34	VSS123	AE64		
BK10	VSS35	VSS124	AE62		
BJ64	VSS36	VSS125	AD57		
BJ21	VSS37	VSS126	AD53		
BJ	VSS38	VSS127	AD50		
BH70	VSS39	VSS128	AD46		
BH67	VSS40	VSS129	AD42		
BH65	VSS41	VSS130	AD4		
BH47	VSS42	VSS131	AC67		
BH24	VSS43	VSS132	AC64		
BH20	VSS44	VSS133	AC10		
BH15	VSS45	VSS134	AC1		
BG51	VSS46	VSS135	AB70		
BG36	VSS47	VSS136	AB62		
BG2	VSS48	VSS137	AB57		
BF30	VSS49	VSS138	AB53		
BF13	VSS50	VSS139	AB50		
BF8	VSS51	VSS140	AB46		
BE70	VSS52	VSS141	AB42		
BE65	VSS53	VSS142	AB39		
BE9	VSS54	VSS143	AB37		
BE1	VSS55	VSS144	AB35		
BD57	VSS56	VSS145	AB33		
BD53	VSS57	VSS146	AB32		
BD50	VSS58	VSS147	AB30		
BD46	VSS59	VSS148	AB28		
BD42	VSS60	VSS149	AB26		
BD39	VSS61	VSS150	AB24		
BD14	VSS62	VSS151	AB23		
BB71	VSS63	VSS152	AB21		
BB62	VSS64	VSS153	AB19		
BB57	VSS65	VSS154	AB17		
BB53	VSS66	VSS155	AB15		
BB50	VSS67	VSS156	AB14		
BB46	VSS68	VSS157	AB9		
BB42	VSS69	VSS158	AA66		
BB39	VSS70	VSS159	AA64		
BB7	VSS71	VSS160	AA62		
BB1	VSS72	VSS161	AA57		
BA70	VSS73	VSS162	AA53		
AY71	VSS74	VSS163	AA44		
AY66	VSS75	VSS164	AA46		
AY62	VSS76	VSS165	AA42		
AY59	VSS77	VSS166	AA39		
AY55	VSS78	VSS167	AA37		
AY51	VSS79	VSS168	AA35		
AY48	VSS80	VSS169	AA33		
AR42	VSS81	VSS170	AA32		
AR39	VSS82	VSS171	AA30		
AR35	VSS83	VSS172	AA28		
AR33	VSS84	VSS173	AA26		
AR30	VSS85	VSS174	AA24		
AR28	VSS86	VSS175	AA23		
AR26	VSS87	VSS176	AA21		
AR24	VSS88	VSS177	AH2		
AR23	VSS89	VSS178	AH57		
AR11	VSS90	VSS179	AH50		
AR19	VSS91	VSS180	AH47		
AR17	VSS92	VSS181	AH46		
AR15	VSS93	VSS182	AH44		
AR14	VSS94	VSS183	AH42		
AR4	VSS95	VSS184	AH41		
AR1	VSS96	VSS185	AH39		
AP70	VSS97	VSS186	AH37		
AP64	VSS98	VSS187	AH36		
AN62	VSS99	VSS188	AH33		
AN65	VSS100	VSS189	AH32		
AY44	VSS101	VSS190	AH30		
AY41	VSS102	VSS191	AH28		
AY37	VSS103	VSS192	AH26		
AY35	VSS104	VSS193	AH24		
AY33	VSS105	VSS194	AH23		
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AY28	VSS108	VSS197	AH17		
AY26	VSS109	VSS198	AH15		
	VSS110	VSS199	AH14		
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	VSS207	VSS296	AD4		
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	VSS235	VSS324	AA66		
	VSS236	VSS325	AA64		
	VSS237	VSS326	AA62		
	VSS238	VSS327	AA57		
	VSS239	VSS328	AA53		
	VSS240	VSS329	AA44		
	VSS241	VSS330	AA46		
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	VSS253	VSS342	AA21		
	VSS254	VSS343	AH2		
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	VSS256	VSS345	AH50		
	VSS257	VSS346	AH47		
	VSS258	VSS347	AH46		
	VSS259	VSS348	AH44		
	VSS260	VSS349	AH42		
	VSS261	VSS350	AH41		
	VSS262	VSS351	AH39		
	VSS263	VSS352	AH37		
	VSS264	VSS353	AH36		
	VSS265	VSS354	AH33		
	VSS266	VSS355	AH32		
	VSS267	VSS356	AH30		
	VSS268	VSS357	AH28		
	VSS269	VSS358	AH26		
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	VSS271	VSS360	AH23		
	VSS272	VSS361	AH21		
	VSS273	VSS			

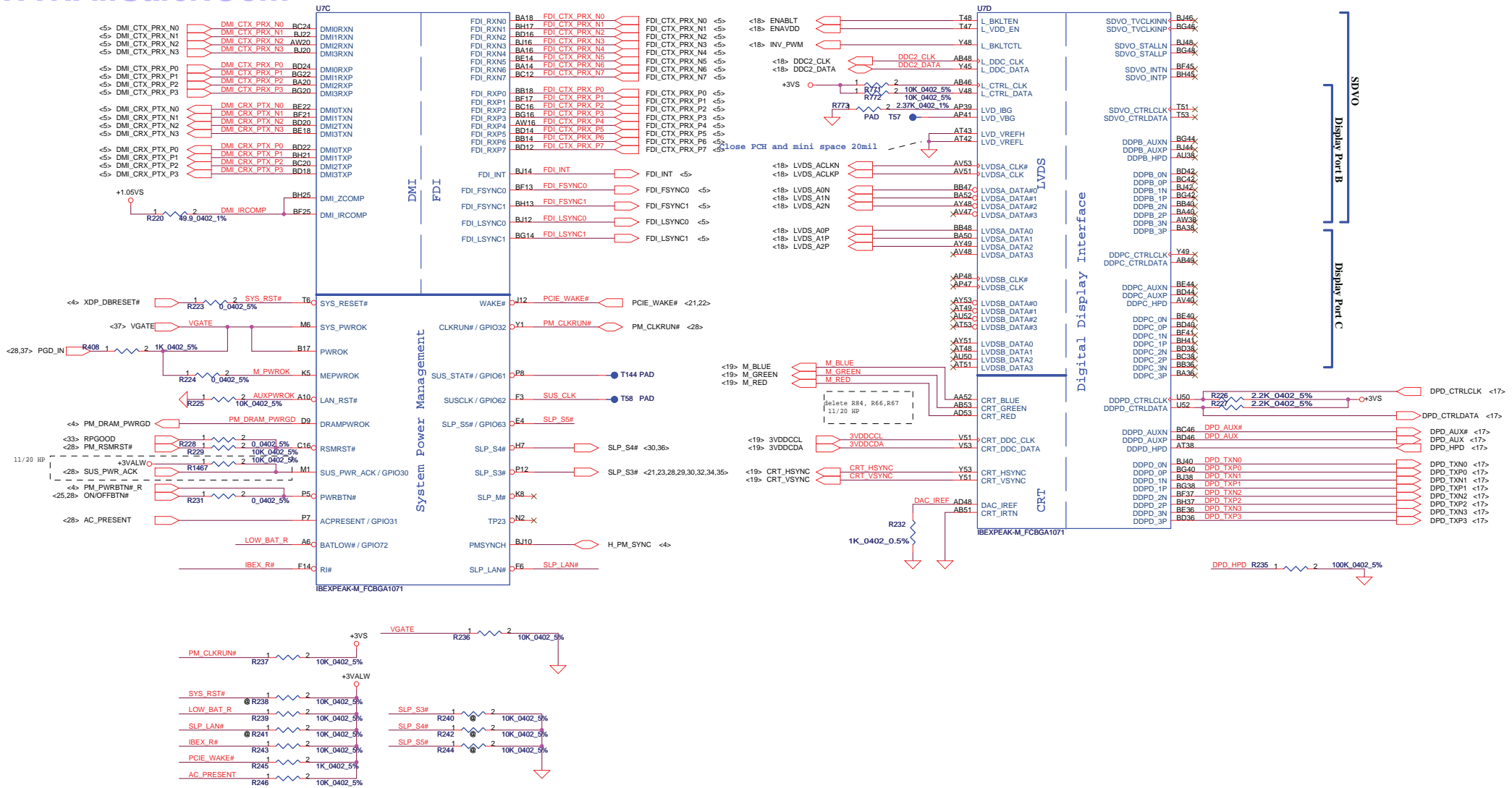


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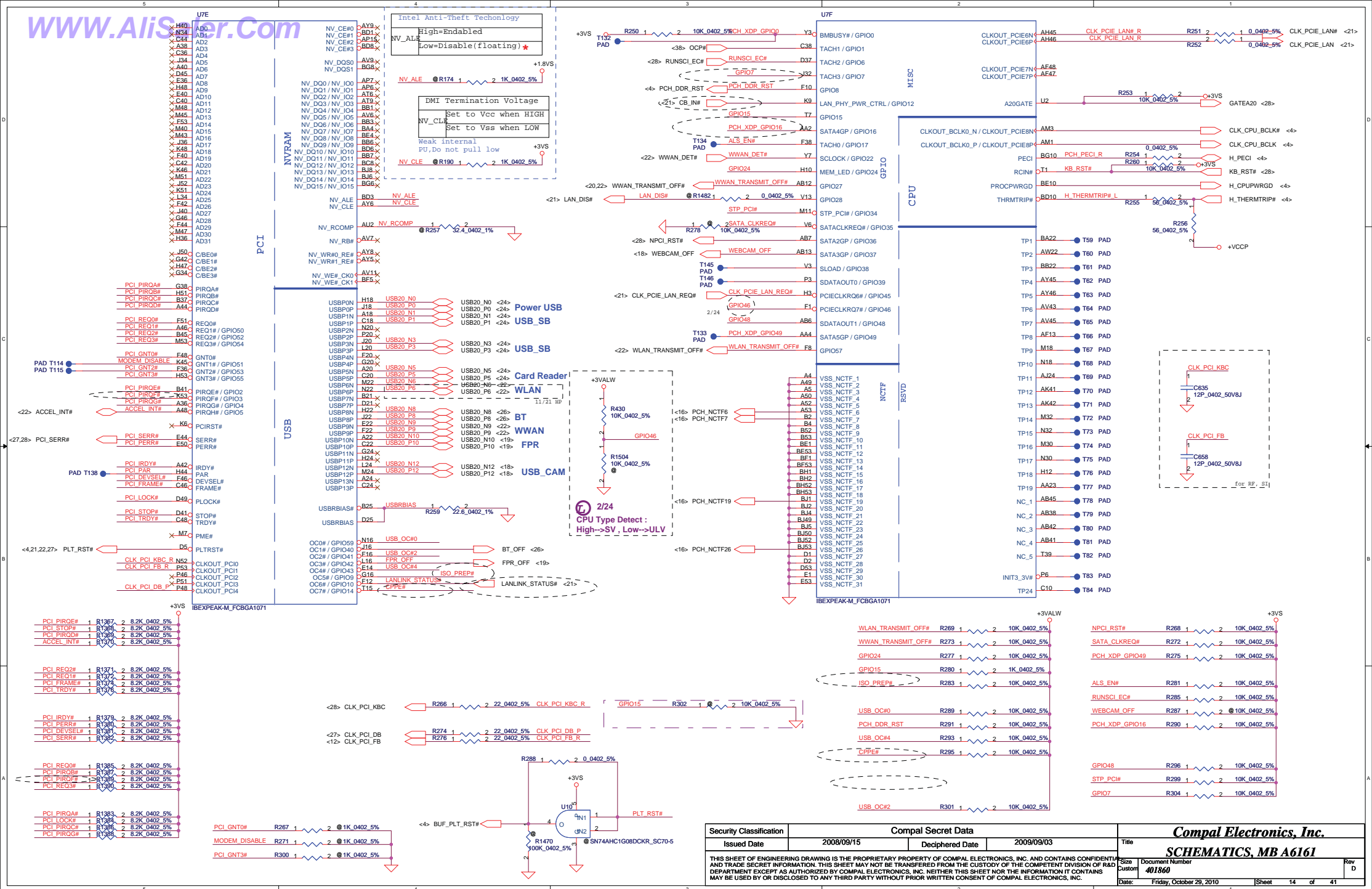


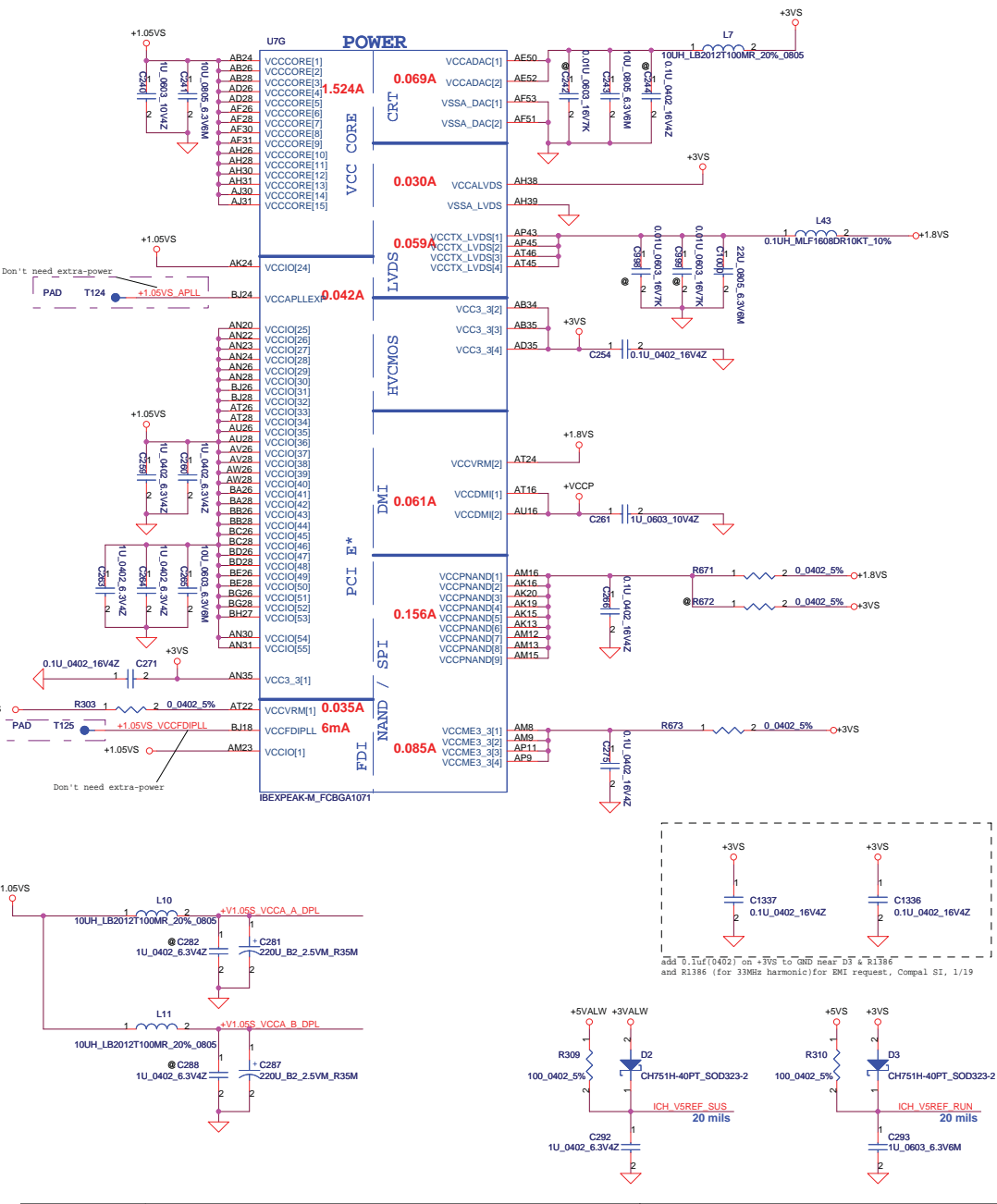
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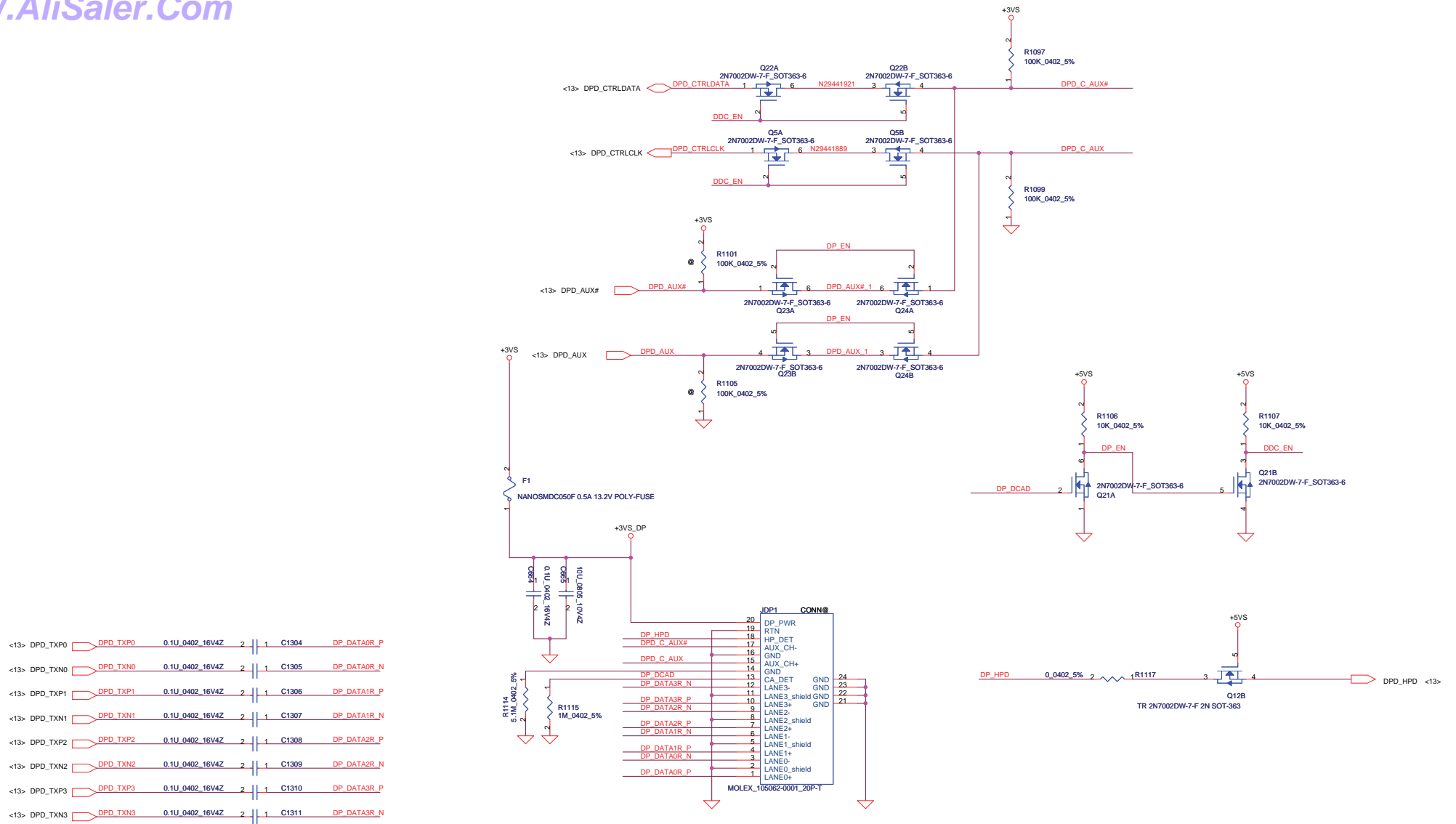


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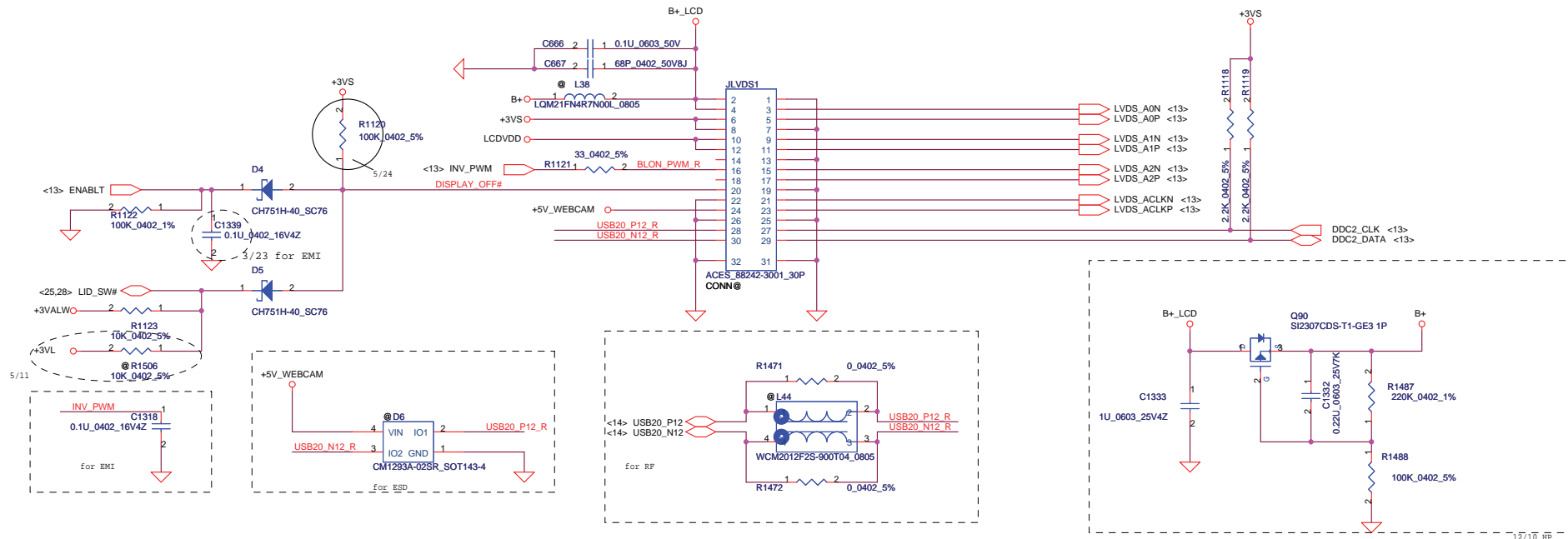


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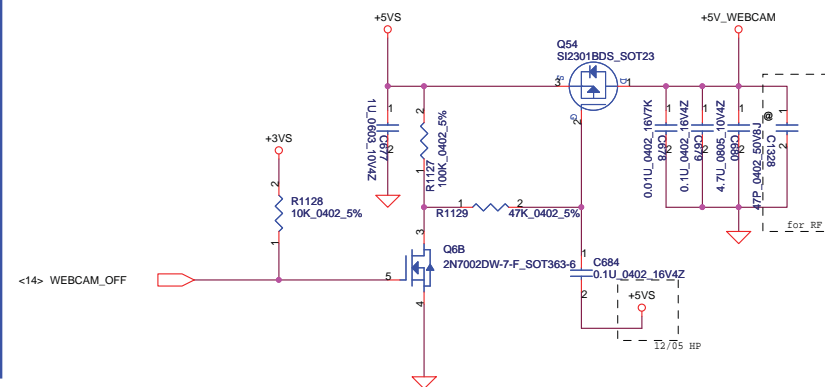
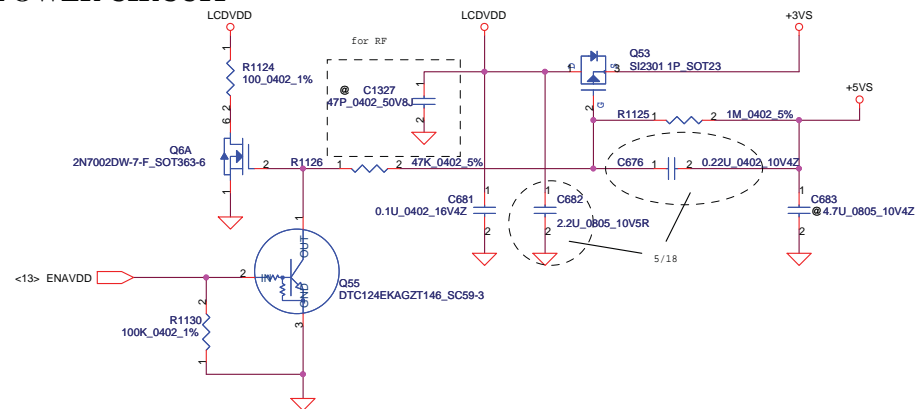


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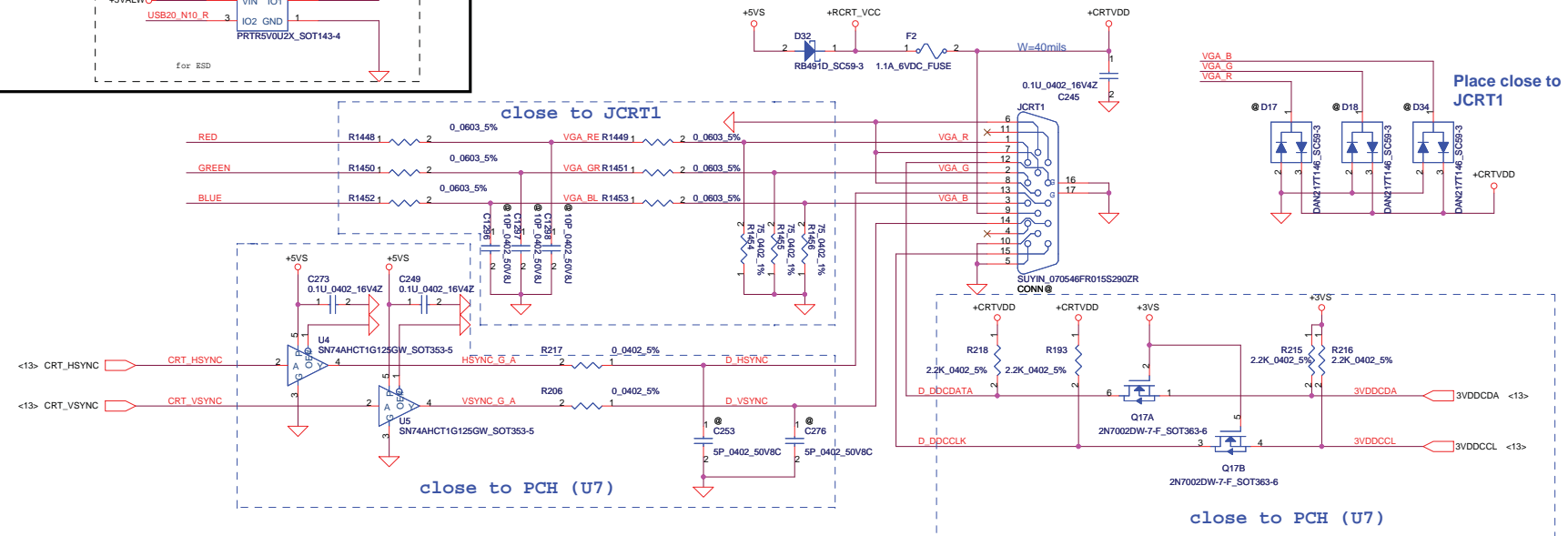
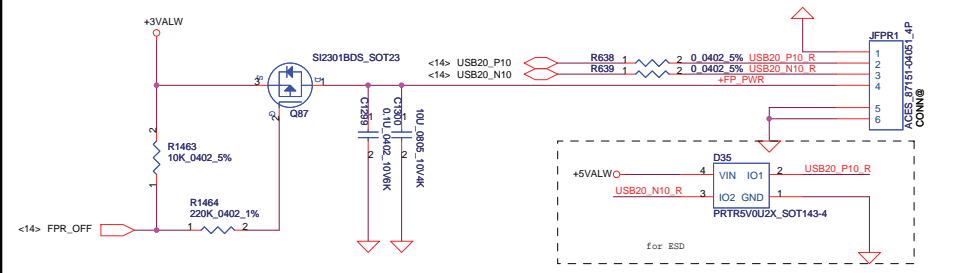
LED/PANEL BD. CONN.



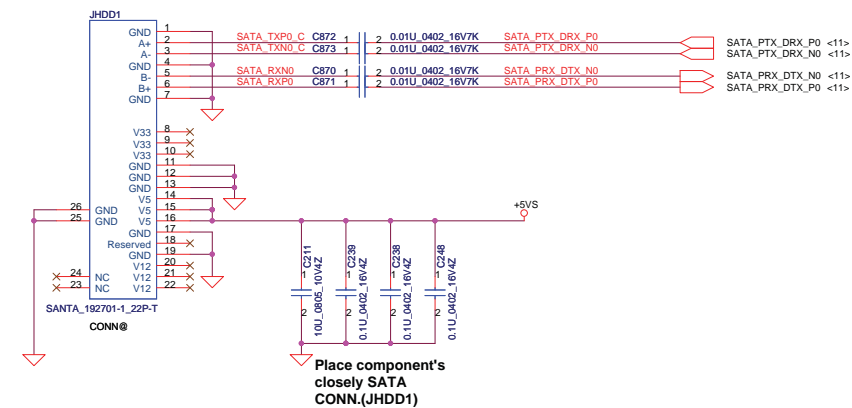
LCD POWER CIRCUIT



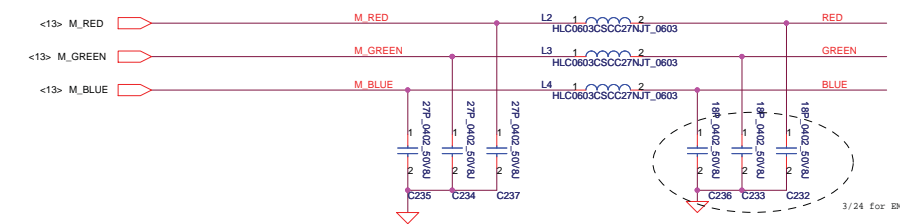
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2.5' SATA HDD Connector

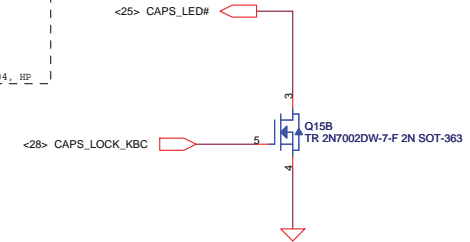
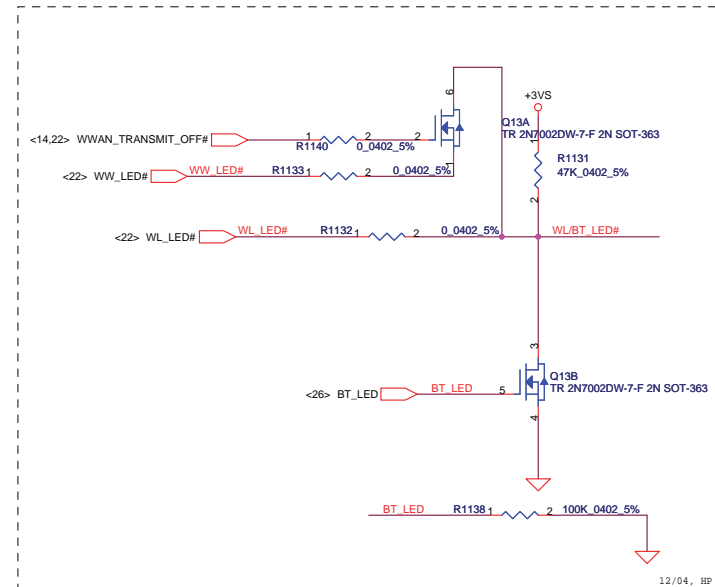
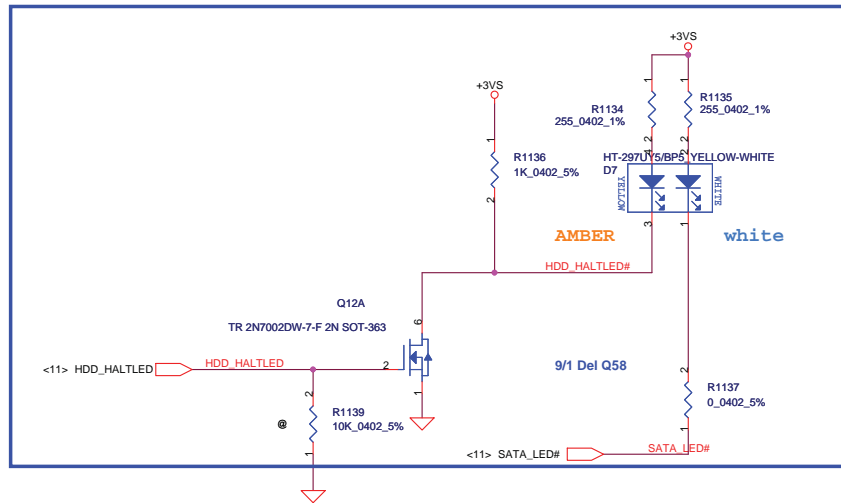


CRT Termination/EMI Filter close to PCH (U7)

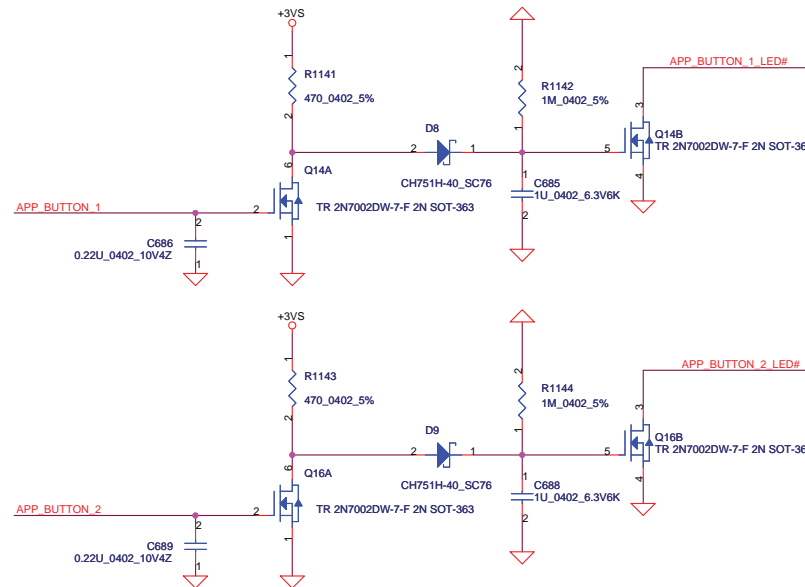
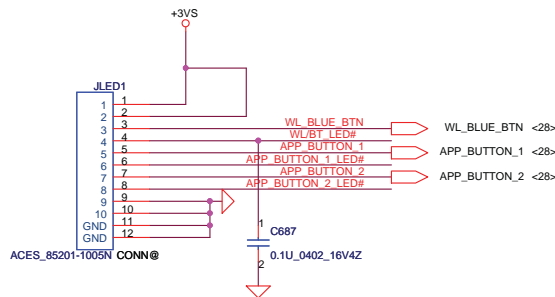


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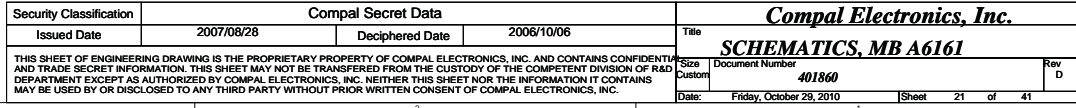
HDD active LED



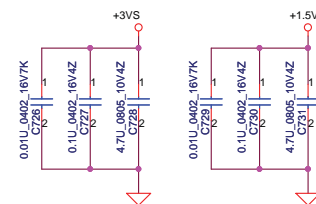
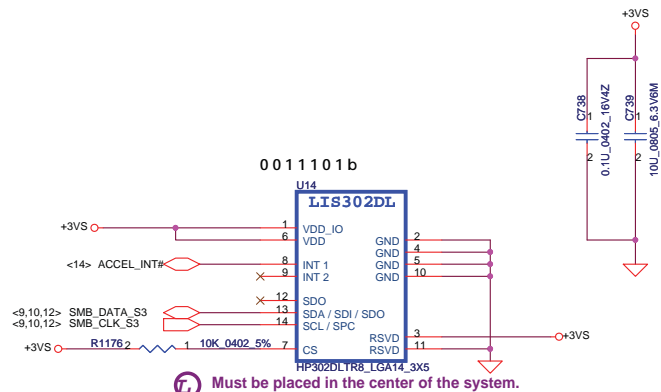
To LED small board



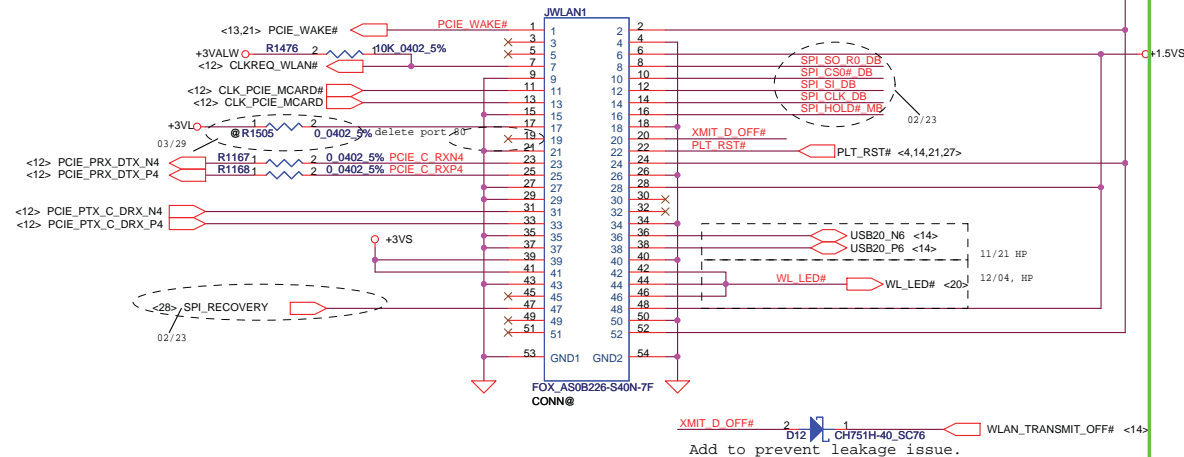
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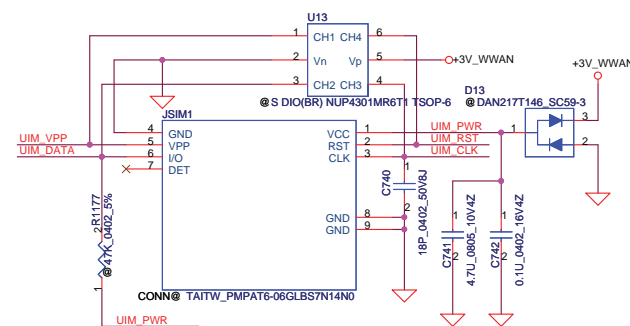
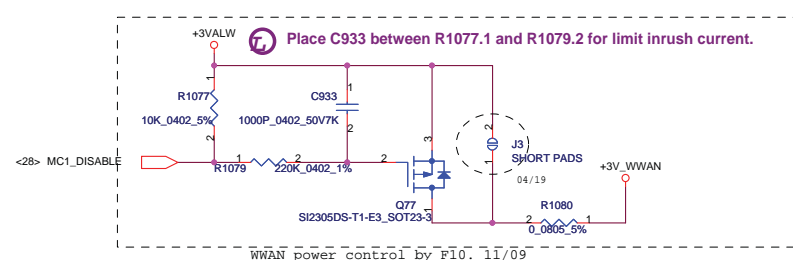
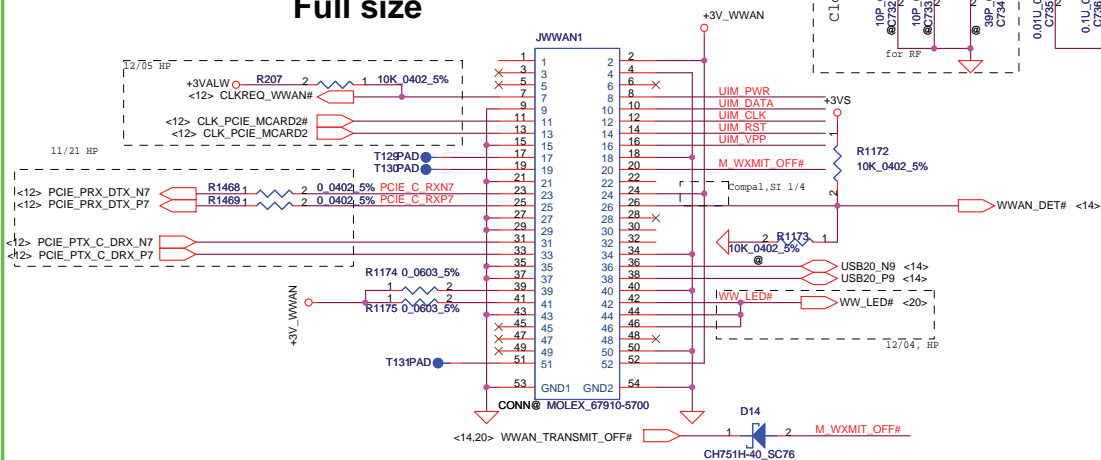
ACCELEROMETER



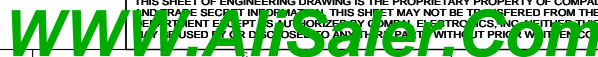
WWLAN/WiMax Mini-Express Card half size

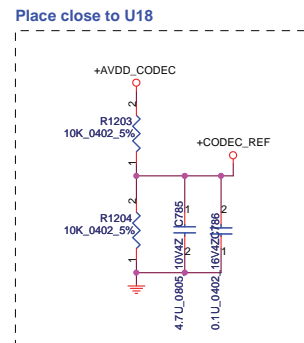


Mini-Express Card--WWAN Full size

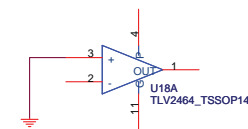
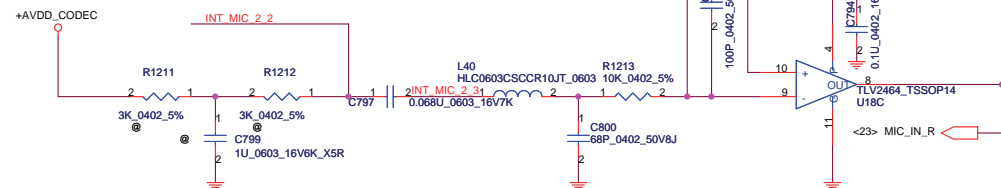


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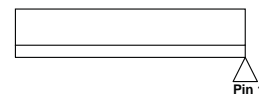
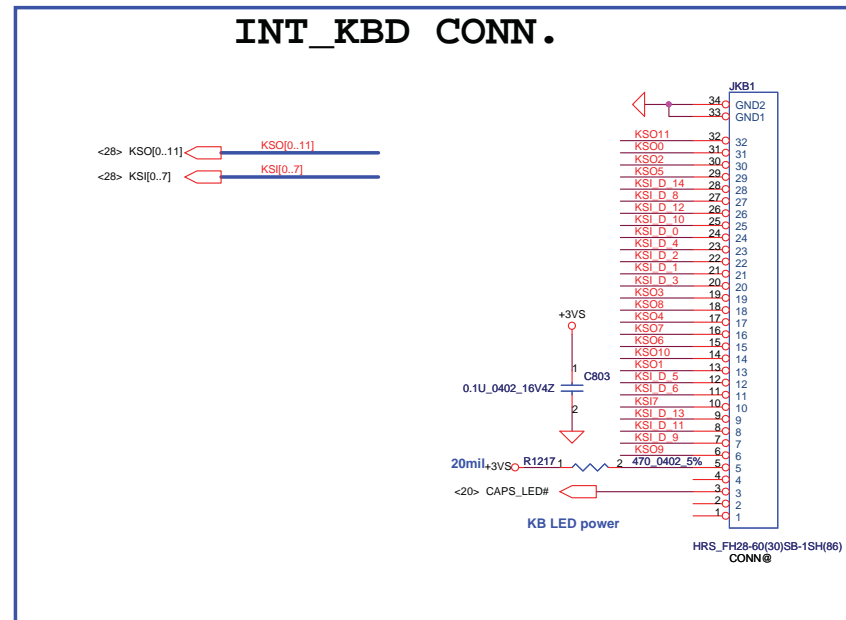


AMP. FOR EXTERNAL MICROPHONE



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SCHEMATICS, MB A6161			
DESIGN OF R&D S	Document Number 401860		Rev D
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INT_KBD CONN.

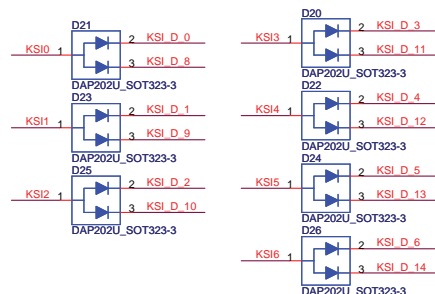
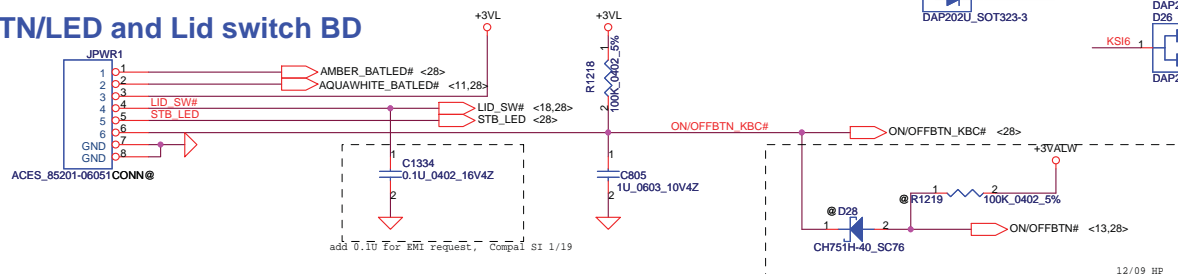


Pin1 reserve , so net name is reserve
for Fossil down contact

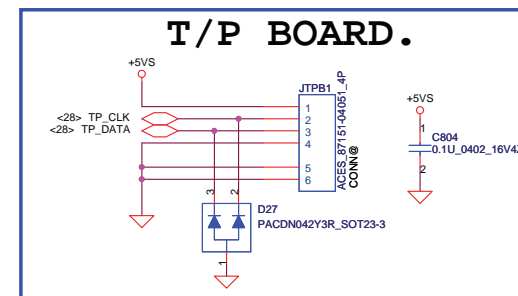
For EMI

KSO11	@ C620	1	2	100P_0402_50V8J
KSO0	@ C656	1	2	100P_0402_50V8J
KSO2	@ C624	1	2	100P_0402_50V8J
KSO5	@ C621	1	2	100P_0402_50V8J
KSI_D_14	@ C622	1	2	100P_0402_50V8J
KSI_D_8	@ C625	1	2	100P_0402_50V8J
KSI_D_12	@ C626	1	2	100P_0402_50V8J
KSI_D_10	@ C627	1	2	100P_0402_50V8J
KSI_D_0	@ C630	1	2	100P_0402_50V8J
KSI_D_4	@ C629	1	2	100P_0402_50V8J
KSI_D_2	@ C631	1	2	100P_0402_50V8J
KSI_D_1	@ C632	1	2	100P_0402_50V8J
KSI_D_3	@ C633	1	2	100P_0402_50V8J
KSO3	@ C634	1	2	100P_0402_50V8J
KSO8	@ C641	1	2	100P_0402_50V8J
KSO4	@ C637	1	2	100P_0402_50V8J
KSO7	@ C657	1	2	100P_0402_50V8J
KSO6	@ C638	1	2	100P_0402_50V8J
KSO10	@ C639	1	2	100P_0402_50V8J
KSO1	@ C640	1	2	100P_0402_50V8J
KSI_D_5	@ C660	1	2	100P_0402_50V8J
KSI_D_6	@ C642	1	2	100P_0402_50V8J
KSI7	@ C659	1	2	100P_0402_50V8J
KSI_D_13	@ C654	1	2	100P_0402_50V8J
KSI_D_11	@ C744	1	2	100P_0402_50V8J
KSI_D_9	@ C745	1	2	100P_0402_50V8J
KSO9	@ C724	1	2	100P_0402_50V8J

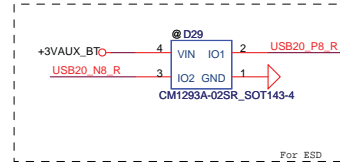
Power BTN/LED and Lid switch BD



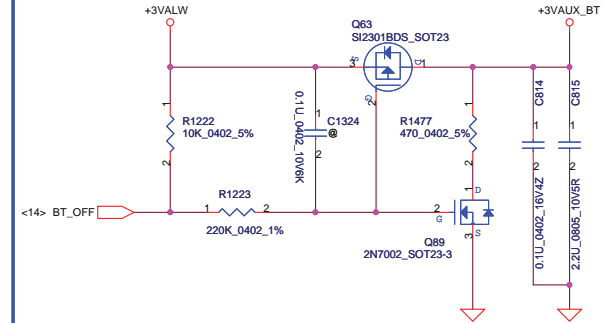
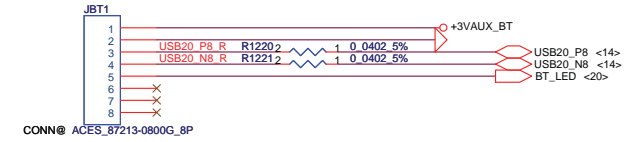
T/P BOARD.



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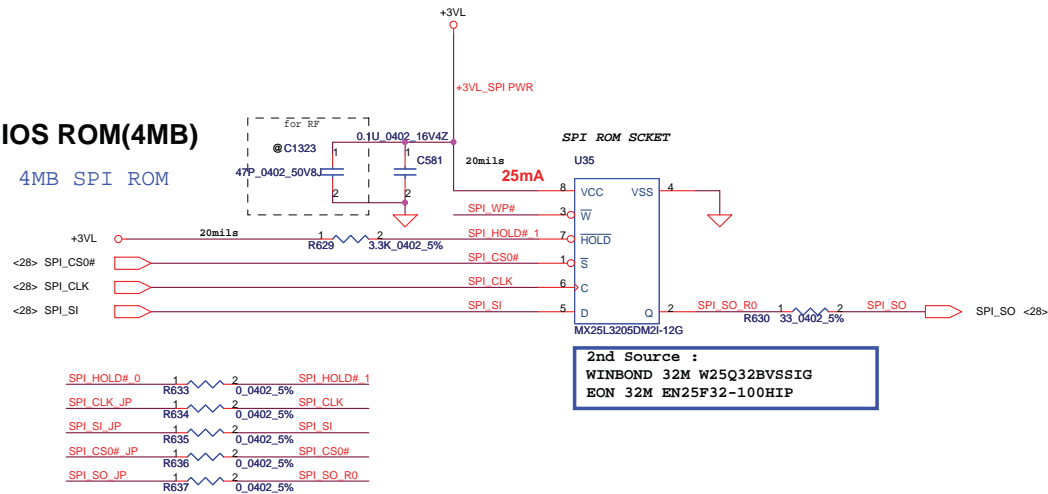
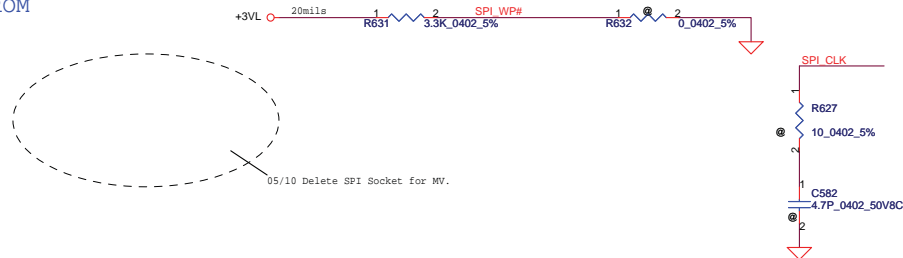
BT(SoftBreeze) Connector



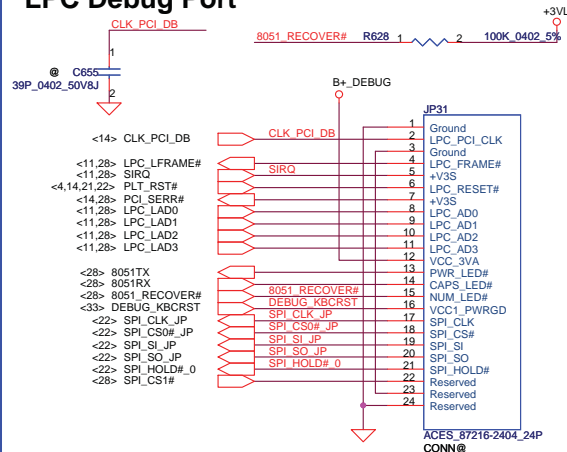
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BIOS ROM(4MB)

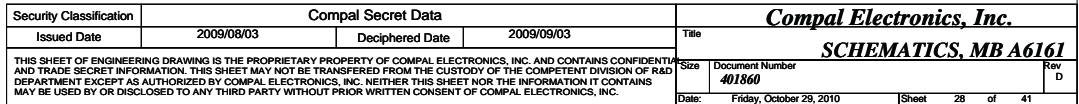
4MB SPI ROM

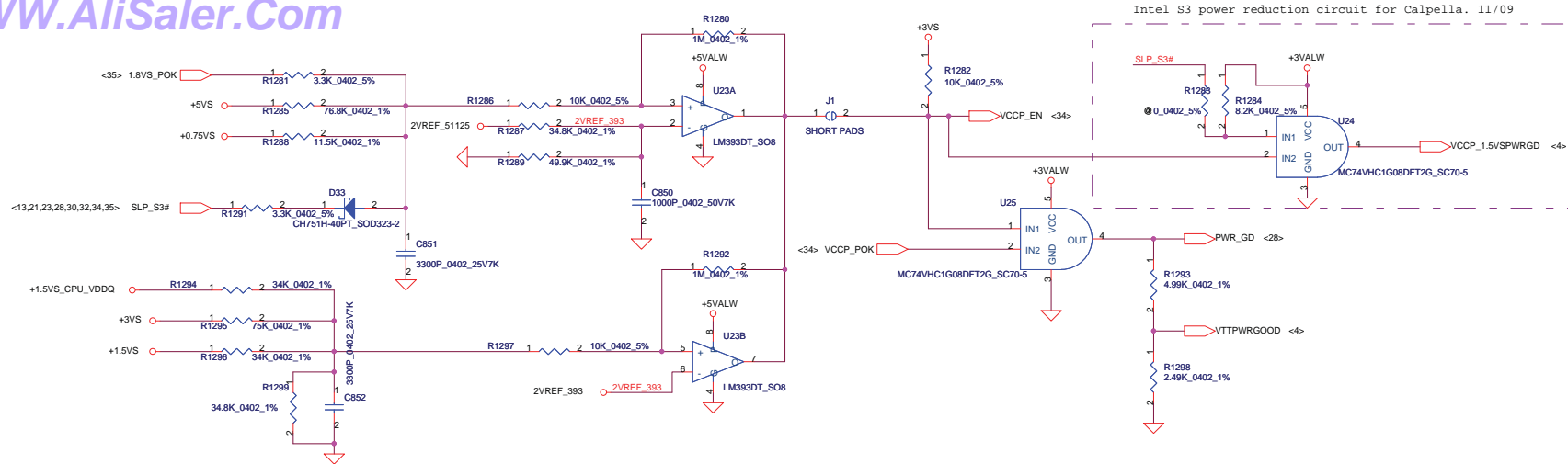
4MB SPI
ROM

LPC Debug Port



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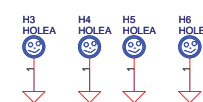




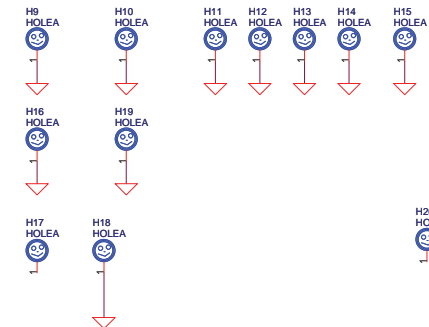
WWAN Card STANDOFF



CPU support

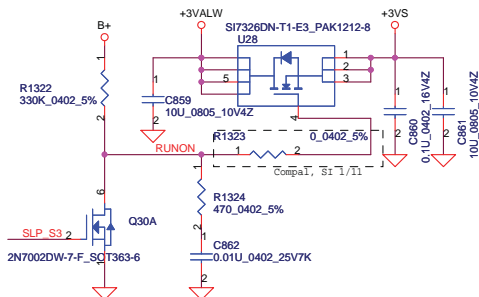


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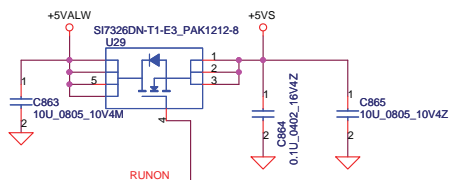


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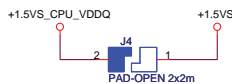
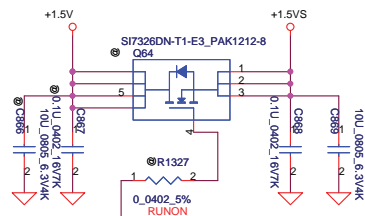
+3VALW to +3VS Transfer



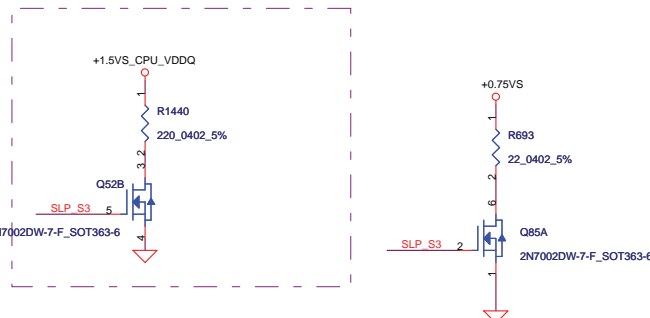
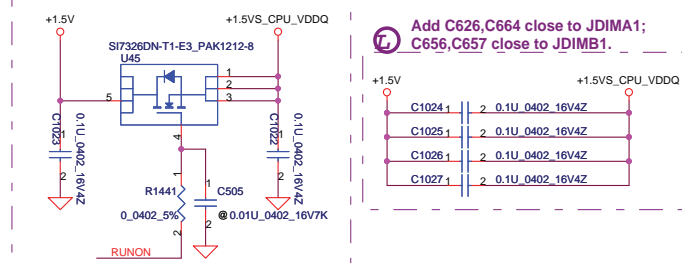
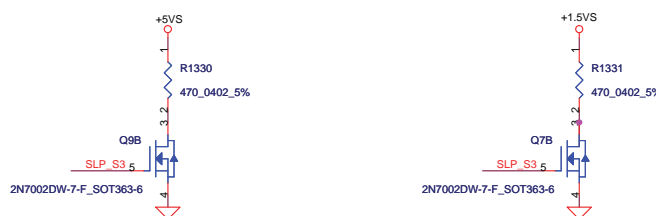
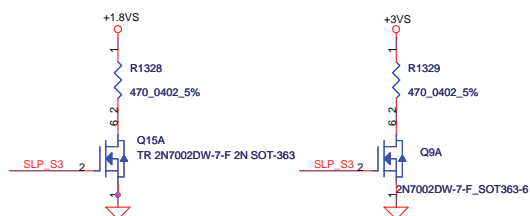
+5VALW to +5VS Transfer



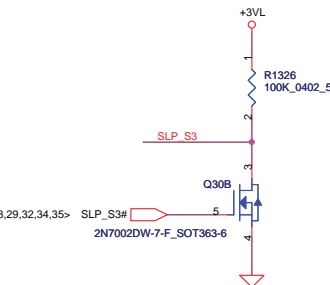
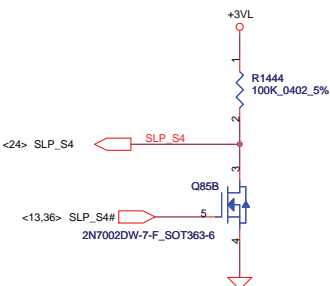
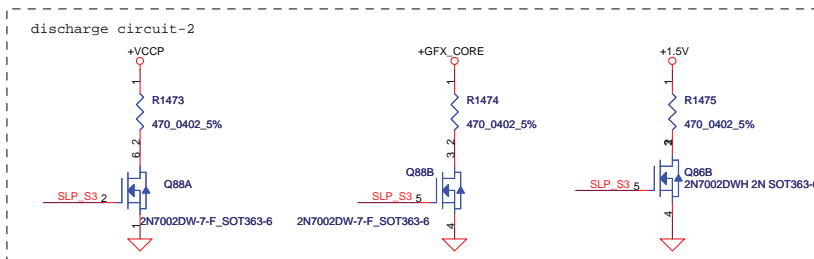
+1.5V to +1.5VS Transfer



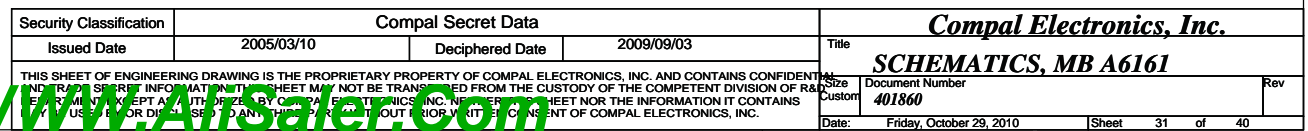
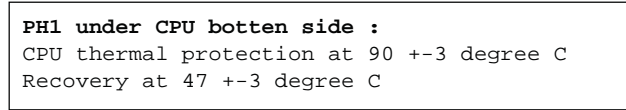
Discharge circuit-1

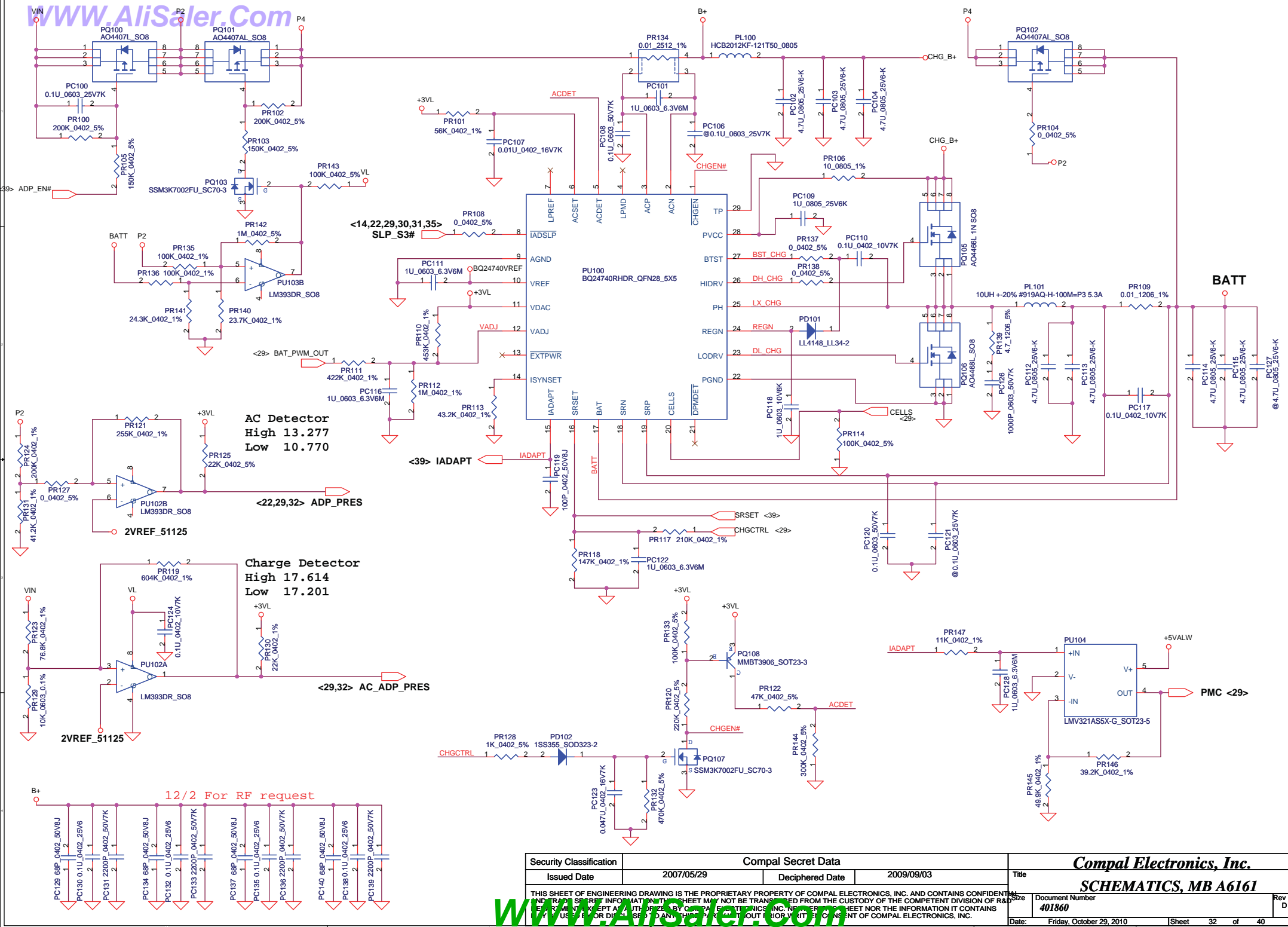


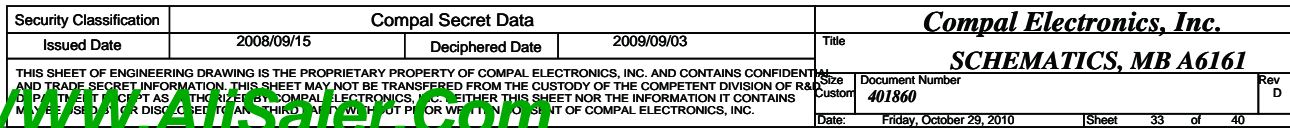
Intel S3 power reduction circuit for Calpella. 11/09

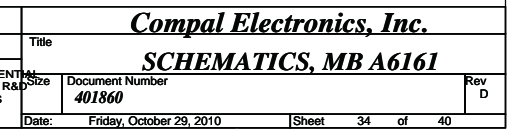


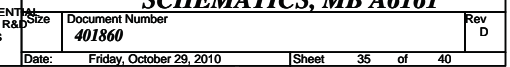
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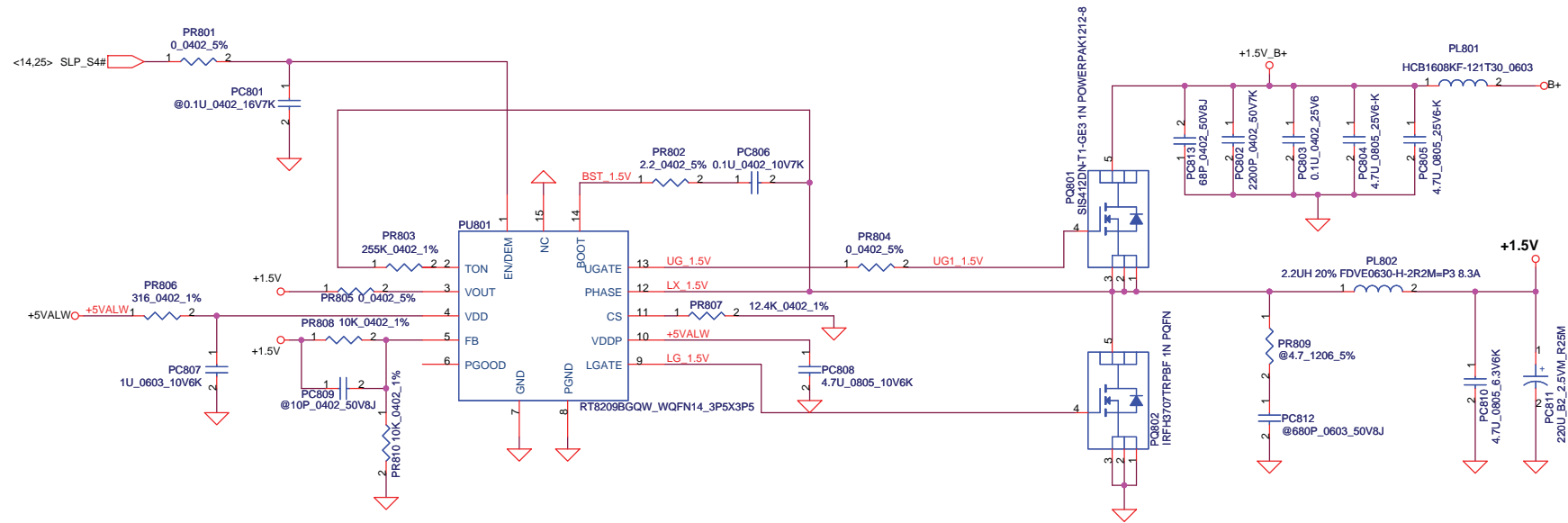








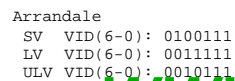




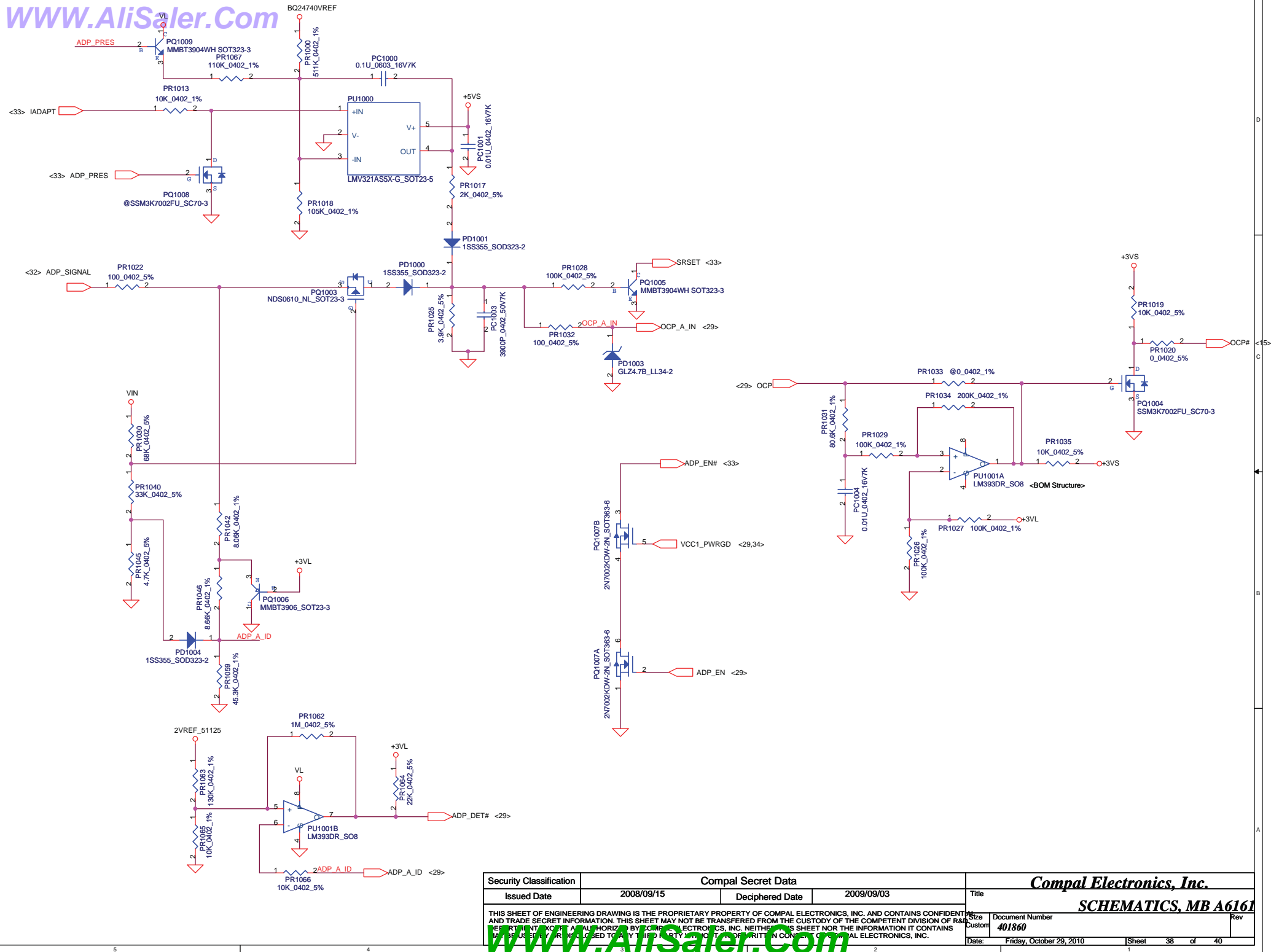
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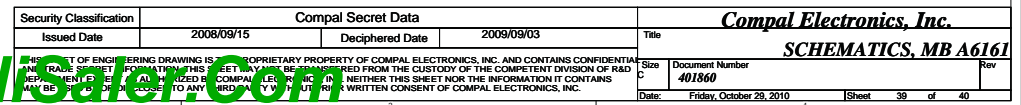
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